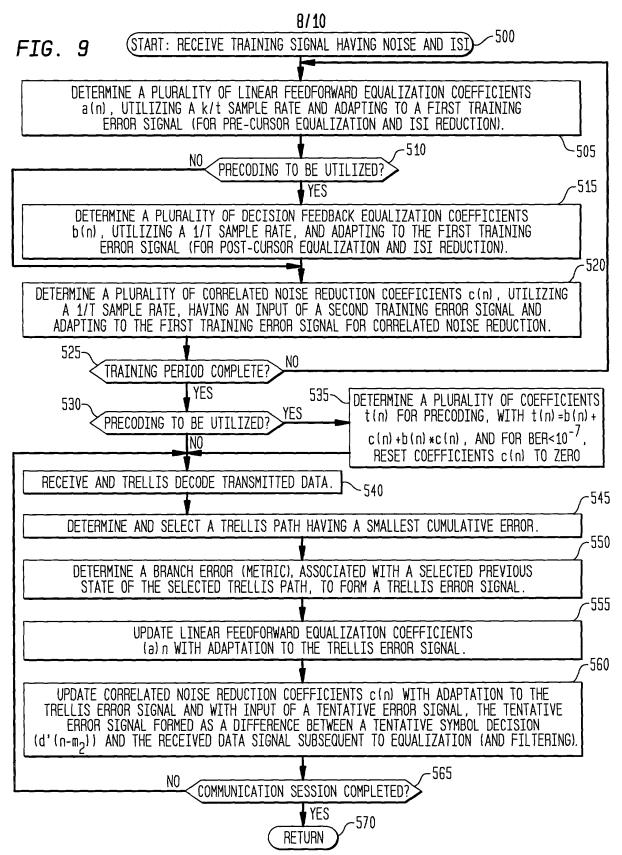


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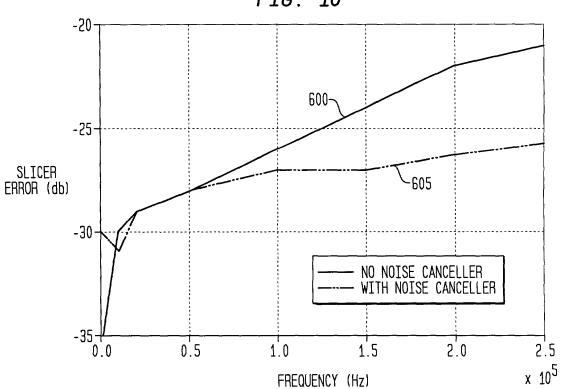
FIG. 8 400 305- \1 **430** TRANSMITTER W/ PRECODER **-440** 415~ NETWORK INTERFACE SYSTEM INTERFACE 450~ -420 105~ LFE **MEMORY** 150-DFE -410 **PROCESSOR** 160~ NOISE PREDICTOR



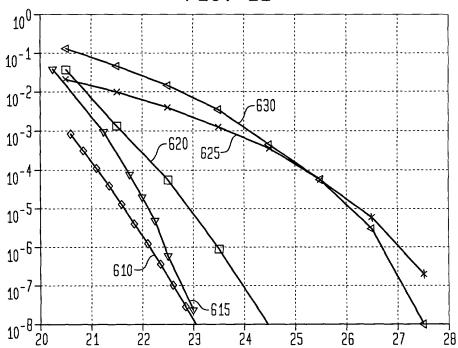
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FIG. 10







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FIG. 12

720~	CROSSTALK ENVIRONMENT	CORRELATION CANCELLER IMPROVED PERFORMANCE MARGIN (db)	<del>ر</del> 735		
700~	24T1+24 HDSL2(C)	1.4			
705~	39 HDSL2(C)	1.5			
710~	24 ADSL+24 HDSL(C)	1.8			
715~	24 T1+24 HDSL2(R)	1.4			